



Information Disclosure Statement
USPN 10/686,545
March 10, 2004
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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-44721NP 620761-8	U.S. SERIAL NO. 10/686,545
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANTS James P. BAUKUS, et al.	
	FILING DATE October 14, 2003	GROUP 2811 2829

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUB- CLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
LT	5,783,846	7/1998	Baukus et al.	257	204	
LT	5,866,933	2/1999	Baukus et al.	257	368	
LT	5,973,375	10/1999	Baukus et al.	257	399	
LT	6,117,762	9/2000	Baukus et al.	438	618	

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

LT	Rovedo, Nivo, et al., "Introducing IBM'S First Cooper Wiring Foundry Technology: Design, Development, and Qualification of CMOS 7SF, a 18- μ m Dual-Oxide Technology for SRAM, ASICs, and Embedded DRAM," MicroNews, Fourth Quarter 2000, Vol 6, No. 4, INTERNET: < http://www-3.ibm.com/chips/micronews/vol6_no4/rovedo.html > 7 pages (February 15, 2002).
LT	Sze, S.M., VSLI Technology, McGraw-Hill, pp. 99, 447, 461-465 (1983).

EXAMINER <i>Uachun</i>	DATE CONSIDERED 4/3/04
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUB- CLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
LF	6,207,479 B1	3/2001	Liew et al.	438	129	
LF	6,413,847, B1	7/2002	Yeh et al	438	598	
LF	2002/0024148 A1	2/2002	Itoh	257	784	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
LF	1 193 758 A1	4/2002	EP			
LF	2001-284357	10/2001	JP			abstract
LF	02/43147 A1	5/2002	WO			abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER <i>Chalman</i>	DATE CONSIDERED 11/3/04
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